

**PAPER 11: SPECIALISATION PAPER V:**  
**LAW OF DESIGNS, INTEGRATED CIRCUITS, GEOGRAPHICAL INDICATIONS**  
**AND CONFIDENTIAL INFORMATION**

**Objectives:**

Designs, integrated circuits, geographical indications and confidential information are modern concepts. All these four types of industrial properties have their own significance in the field of intellectual property. While designs are used in attracting the customers; integrated circuits are required to distinguish one company from another in devising and integrating the machines and apparatus; geographical indications indicate the prominence of the products which carry the essence of the climatic conditions of the particular region or geographical area; and confidential information meant for protecting the interest of the manufacturer/employer from disclosure of their trade secrets. In modern days legal framework has been devised to govern and regulate these types of IPRs at national and international level. Students are required to acquaint themselves by studying the national and international legal regimes governing the designs, integrated circuits, geographical indications, and confidential information in a detailed manner.

**Course Outcomes:**

On successful completion of the course the student will be able to –

1. demonstrate evolution and growth of of designs, integrated circuits, and geographical indications law in India
2. explore the interrelation between designs, integrated circuits, geographical indications and confidential information
3. analyse the basic requirements for protection of designs, geographical indications and integrated circuits
4. give practical advice on the remedies available for violation of the rights relating to these intellectual properties.

**Course Content:**

**UNIT: I** – Indian and international history of designs, integrated circuits, geographical indications and confidential information, significance of these concepts in modern times of socio-economic, technological and industrial growth; interface between designs, integrated circuits, geographical indications and confidential information.

**UNIT: II** – Basic principles of Designs, the salient features of the Designs Act, 2000, Registrable and Non-registrable designs, procedure for registration, rights of holder, design piracy, remedies.

**UNIT: III** - Basic principles of GI, The Geographical Indications of Goods (Registration And Protection) Act, 1999- salient features, Background of the enactment, meaning and definitions of geographical indications, registered proprietor, authorised user, deceptively

similar, and producer; procedure and duration of registration, effect of registration, rectification and correction of register; infringement and remedies: civil and criminal.

**UNIT: IV-** Confidential Information and Trade Secrets: Meaning and nature of confidential information and Trade Secrets, difference between trade secret and confidential information, Nature of trade secrets, Modes of protection of trade secrets- legislative measures, Technology Transfer Agreements, non-disclosure agreements.

Semiconductor Integrated Circuits Layout Designs Act, 2000- Salient features of the Act, Conditions and Procedure for Registration, Duration and Effect of Registration, Assignment and Transmission.

**UNIT: V** –Salient features of international legal instruments: Paris Convention for the Protection of Industrial Property, 1883, TRIPs Agreement, Hague Industrial Design Treaty, 1959; Hague Agreement Concerning the International Deposit of Industrial Designs, 1925; Locarno Agreement: Locarno Agreement Establishing an International Classification for Industrial Designs, 1968, Lisbon Agreement for the Protection of Appellations of Origin and their International Registration, 1958, Madrid Agreement for the Repression of False or Deceptive Indications of Source on Goods, 1891, Treaty on Intellectual Property in Respect of Integrated Circuits, 1989.

**Prescribed Books:**

1. William Cornish and David Llewelyn, Intellectual Property: Patents, Copyright, Trade Marks and Allied Rights, Sweet and Maxwell, London (2003).
2. L. Bently and B. Sherman, Intellectual Property Law, Oxford University Press, Oxford.
3. Ranabhir Singh, Law relating to Intellectual Property, Vols. 1-3, Universal Law Publishing Co. Pvt. Ltd., Delhi.
4. Andrew Christie, Integrated Circuits and their Contents: International Protection, Sweet and Maxwell, London (1995).
5. Elizabeth Verky, Intellectual Property, Eastern Book Publication Co., Nagpur.
6. V. K. Ahuja, Intellectual Property Rights, Lexis Nexis, New Delhi.
7. Lionel Bently & Brad Sherman, Intellectual Property Law, Oxford University Press, 3rd edn., 2008.

**Reference Books:**

1. Hilary E. Pearson and Clifford G. Miller, Commercial Exploitation of Intellectual Property, Universal Law Publishing Co. Pvt. Ltd., Delhi (1997).
2. Department of Business Laws, NLSIU, Bangalore, International Legal Instruments of Intellectual Property Rights Law, Vols. I&II.
3. Ashok Soni, A Complete Reference of Intellectual Property Laws, Vols. 1&2.
4. Bibek Debroy (Ed.), Intellectual Property Rights, B.R. Publishing Corporation (India) Ltd. (1998).
5. R. Anita Rao and V. Bhanoji Rao, Intellectual Property Rights: A Primer, Eastern Book Company, Lucknow (2008).
6. W.R. Cornish, Intellectual Property: Patents, Copyright, Trade Marks and Allied Rights, Universal Law Publishing Co. Pvt. Ltd., Delhi (2001).